



Application of: BRADLEY J. HOWARD

Filed:

DECEMBER 13, 2001

For:

PROCESS FOR USING PHOTO-DEFINABLE LAYERS IN THE

MANUFACTURE OF SEMICONDUCTOR DEVICES AND

RESULTING STRUCTURES OF SAME

Serial No.:

10/015,434

Group Art Unit:

2823

Examiner:

NGUYEN, K.

Atty Dkt:

97-0008.01

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

Date

Name

Commissioner For Patents P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

RESPONSE TO OFFICE ACTION MAILED JUNE 24, 2005

This paper is submitted in response to the office action dated June 24, 2005. Favorable reconsideration is respectfully requested.

USSN: 10/015,434